Docket No.

203056US2RD/s

TES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

MOTO, et al.

SERIAL NO:

09/781.158

FILED:

February 13, 2001

TAMPER RESISTANT MICROPROCESSOR

GAU:

EXAMINER:

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

FOR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☐ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- □ Each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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(OSMMN 10/98)

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Group Art Unit:

JUN 5 2001

STATEMENT OF RELEVANCY

Reference AA (US 5,805,706) on Form 1449:

This reference discloses a cryptographic device formed as an integrated circuit encapsulated in an integrated circuit package.

Reference AB (US 6,003,117) on Form 1449:

This reference discloses an integrated circuit accesses encrypted data stored in an external memory, the integrated circuit utilizes the decrypted data in the main memory.

Reference AC (US 5,123,045) on Form 1449:

This reference discloses an efficient software protection scheme is presented in which a data processing system provides comprehensive software protection using hardware and software measures.

Reference AO (JP 11-282667) on Form 1449:

This reference discloses a microprocessor enabled to execute plural programs encrypted by individual keys.

Reference AP (JP 2980576) on Form 1449:

This reference discloses a random number generator using physical noise generator and A/D converter.

Reference AW on Form 1449:

This reference is discussed in the specification.

Reference AX on Form 1449:

This reference discloses an explanation of tamper resistant software.